

METHOD AND SYSTEM FOR MAINTAINING DATA AT INPUT/OUTPUT
(I/O) INTERFACES FOR A MULTIPROCESSOR SYSTEM

ABSTRACT OF THE DISCLOSURE

A multiprocessor system and method includes a processing sub-system having a plurality of processors and a processor memory system. A scalable network is operable to couple the processing sub-system to an input/output (I/O) sub-system. The I/O sub-system includes a plurality of I/O interfaces. Each I/O interface has a local cache and is operable to couple a peripheral device to the multiprocessor system and to store copies of data from the processor memory system in the local cache for use by the peripheral device. A coherence domain for the multiprocessor system includes the processors and processor memory system of the processing sub-system and the local caches of the I/O sub-system.

PCT/US2007/033309